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1	(hdl vhdl hardware with description with language) with (reus\$5 re adj us\$5) and @ad<"20030804" and ( hlms high with level with model\$5) and simulat\$5 with multiplex\$5 and synthes\$6
2	(hdl vhdl hardware with description with language) with (reus\$5 re adj us\$5) and @ad<"20030804" and ( hlms high with level with model\$5)
3	(hdl vhdl hardware with description with language) with (reus\$5 re adj us\$5) and @ad<"20030804" and ( hlms high with level with model\$5) and user
4	(hdl vhdl hardware with description with language) with (reus\$5 re adj us\$5) and @ad<"20030804" and ( hlms high with level with model\$5) and user with (defin\$6 configur\$6 select\$7 specif\$5) with attribute
5	(hdl vhdl hardware with description with language) with (reus\$5 re adj us\$5) and @ad<"20030804" and ( hlms high with level with model\$5) and user same attribute
6	(hdl vhdl hardware with description with language) with (reus\$5 re adj us\$5) and @ad<"20030804" and ( hlms high with level with (model\$5 simulat\$4 synthes\$6) ) and user same attribute
7	(hdl vhdl hardware with description with language) with (reus\$5 re adj us\$5) and @ad<"20030804"
8	(hdl vhdl hardware with description with language) with (reus\$5 re adj us\$5) and @ad<"20030804" and simulat\$4 and synthes\$6
9	(hdl vhdl hardware with description with language) with (reus\$5 re adj us\$5) and @ad<"20030804" and simulat\$4 and synthes\$6 and user same attribute
10	(hdl vhdl hardware with description with language) with (reus\$5 re adj us\$5) and @ad<"20030804" and simulat\$4 and synthes\$6 and (hdl vhdl hardware with description with language) same (top\$7 high root) near3 level
11	(hdl vhdl hardware with description with language) with (reus\$5 re adj us\$5) and @ad<"20030804" and simulat\$4 and synthes\$6 and (hdl vhdl hardware with description with language) same (top\$7 high root upper\$7) near3 level
12	(hdl vhdl hardware with description with language) with (reus\$5 re adj us\$5) and @ad<"20030804" and simulat\$4 and synthes\$6 and (hdl vhdl hardware with description with language) same (top\$7 high root upper\$7) near3 level and librar\$5
13	(hdl vhdl hardware with description with language) with (reus\$5 re adj us\$5) and @ad<"20030804" and simulat\$4 and synthes\$6 and (hdl vhdl hardware with description with language) same (top\$7 high root upper\$7) near3 level and librar\$5 and user adj3 (defin\$5 configur\$6 select\$5 specif\$7 attribute)
14	(hdl vhdl hardware with description with language) with (reus\$5 re adj us\$5) with (code software program routine subroutine sub adj routine) and @ad<"20030804" and simulat\$4 and synthes\$6 and (hdl vhdl hardware with description with language) same (top\$7 high root upper\$7) near3 level and librar\$5 and user adj3 (defin\$5 configur\$6 select\$5 specif\$7 attribute)
15	(hdl vhdl hardware with description with language) with (old) and @ad<"20030804" and simulat\$4 and synthes\$6 and (hdl vhdl hardware with description with language) same (top\$7 high root upper\$7) near3 level and librar\$5 and user adj3 (defin\$5 configur\$6 select\$5 specif\$7 attribute)
16	"6425116".pn. and HDL same (high root parent top\$7 upper\$7) same level
17	"6425116".pn. and HDL same (behav\$6 high root parent top\$7 upper\$7) same level

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18	"6425116".pn. and (hdl vhdl hardware with description with language) same (behav\$6 high root parent top\$7 upper\$7) same level
19	"6425116".pn. and (hdl vhdl hardware with description with language) same (reus\$5 re adj us\$5)
20	"6425116".pn. and simulat\$5
21	"6425116".pn. and simulat\$5 and user near3 (defin\$5 configur\$6 select\$5 specif\$7 attribute) and subsystem
22	"6425116".pn. and simulat\$5 and subsystem
23	"6425116".pn. and simulat\$5 same user
24	"20010034876" and hdl same (high top\$7 root\$5 upper\$8) same (user sub\$7)
25	"20010034876" and hdl same (generat\$4 creat\$4)
26	(hdl vhdl hardware with description with language) with (reus\$5 re adj us\$5) and @ad<"20030804" and simulat\$4 and synthes\$6 and (user designer engineer) same (hdl vhdl hardware with description with language)
27	(hdl vhdl hardware with description with language) same (simulat\$4 synthes\$5) and @ad<"20030804" and (user designer engineer) same (hdl vhdl hardware with description with language)
28	(hdl vhdl hardware with description with language) same (simulat\$4 synthes\$5) and @ad<"20030804" and (user designer engineer) same (hdl vhdl hardware with description with language) and (mux\$3 multiplexer)
29	(hdl vhdl hardware with description with language) same (simulat\$4 synthes\$5) and @ad<"20030804" and (user designer engineer) near2 (config\$6 defin\$6 specif\$6 requir\$6 select\$6 input) same (hdl vhdl hardware with description with language) and (mux\$3 multiplexer)
30	(hdl vhdl hardware with description with language) same (simulat\$4 synthes\$5) and @ad<"20030804" and (user designer engineer) near2 (config\$6 defin\$6 specif\$6 requir\$6 select\$6 input) same (hdl vhdl hardware with description with language) and (mux\$3 multiplexer) and (hdl vhdl hardware with description with language) with (reus\$5 re adj us\$5)
31	(hdl vhdl hardware with description with language) same (simulat\$4 synthes\$5) and @ad<"20030804" and (user designer engineer) near2 (config\$6 defin\$6 specif\$6 requir\$6 select\$6 input) same (hdl vhdl hardware with description with language) and (mux\$3 multiplexer) and (hdl vhdl hardware with description with language) same (chang\$6 unchang\$5 wiithout compatib\$5)
32	(hdl vhdl hardware with description with language) same (simulat\$4 synthes\$5) and @ad<"20030804" and (user designer engineer) near2 (config\$6 defin\$6 specif\$6 requir\$6 select\$6 input) same (hdl vhdl hardware with description with language) and (mux\$3 multiplexer) and (hdl vhdl hardware with description with language) same (old)
33	(hdl vhdl hardware with description with language) same (simulat\$4 synthes\$5) and @ad<"20030804" and (user designer engineer) near2 (config\$6 defin\$6 specif\$6 requir\$6 select\$6 input) same (hdl vhdl hardware with description with language) and (mux\$3 multiplexer) and (hdl vhdl hardware with description with language) same (previous\$4 old) same (generat\$4 creat\$4 code program routine software algorithm)